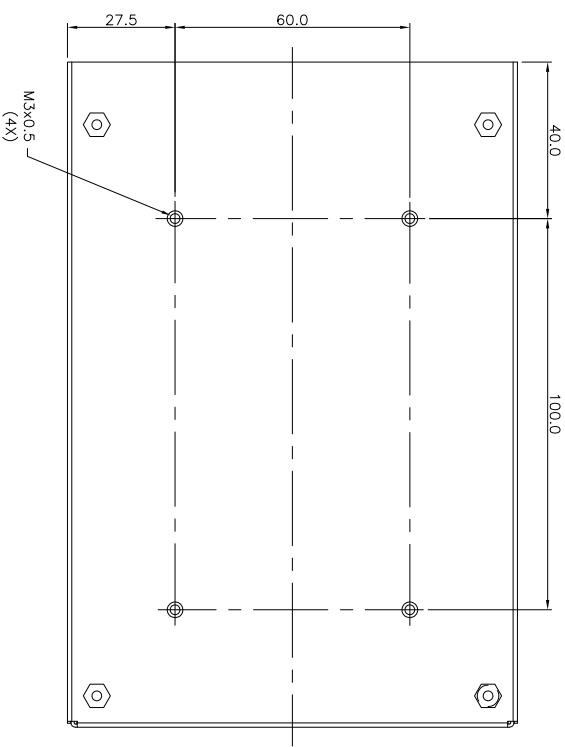
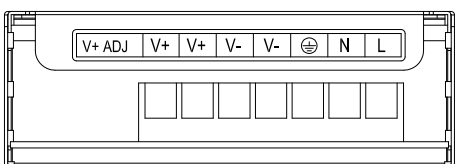
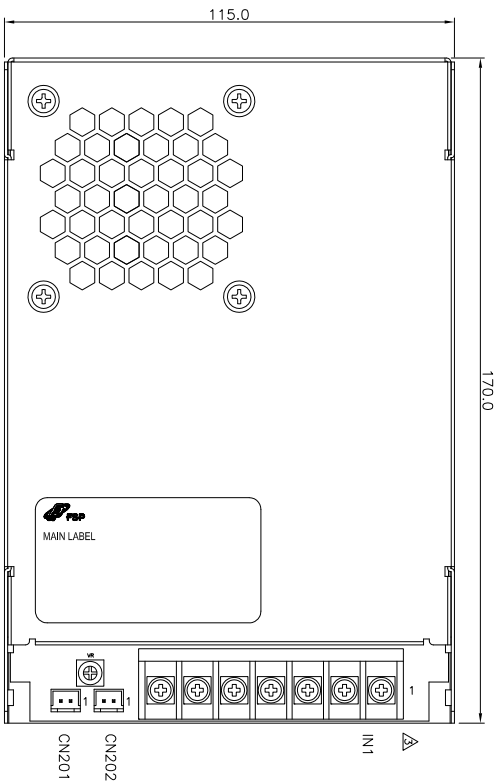
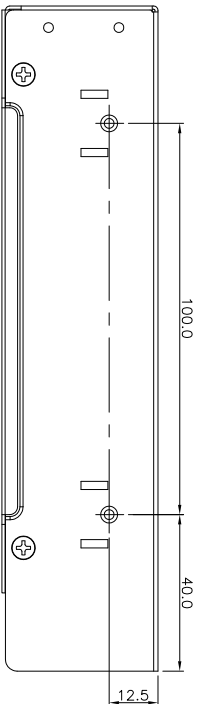


備註：產地標籤依業務指示加貼。

Pin assignment of IN1		Wafer
Pin No.	Function	DINKLE DT-49-C91W-07 or EQUIVALENT
1	AC/L	
2	AC/N	
3	FG	
4	V-	
5	V-	
6	V+	
7	V+	

Pin assignment of CN201		Wafer
Pin No.	Function	JST B2B-XH-A or EQUIVALENT
1	PS_OFF	
2	PS_OFF RTN	

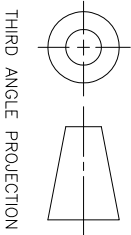
Pin assignment of CN202		Wafer
Pin No.	Function	JST B2B-XH-A or EQUIVALENT
1	Power Good	
2	Power Good RTN	

SCALE:
UNIT: mm

DIMENSIONAL TOLERANCES

HOLES : ± 0.2 ANGLES : $\pm 0.5^\circ$

(V)	()	()	()	()
<30	± 0.25	DECIMALS	UP~100 : ± 0.2	250~300 : ± 0.4
>30~100	± 0.35	X	± 0.3	100~150 : ± 0.25
>100~300	± 0.5	X X	± 0.2	150~200 : ± 0.3
ABOVE 300	± 0.6	X X X	± 0.1	200~250 : ± 0.35
				300~350 : ± 0.45
				100~600 : ± 1.0
				600~900 : ± 2.4
				900~OVER : ± 3.1



THIRD ANGLE PROJECTION

MODEL NO. : FSP300-PBA-A24 TITLE: ASSY

R&D

P E

DRAWN

DATE

SHEET: 1 OF 1 REV: 03

P/N.: 90U3004300

JUN.19.2025

INTERIOR COUNTERSIGN: